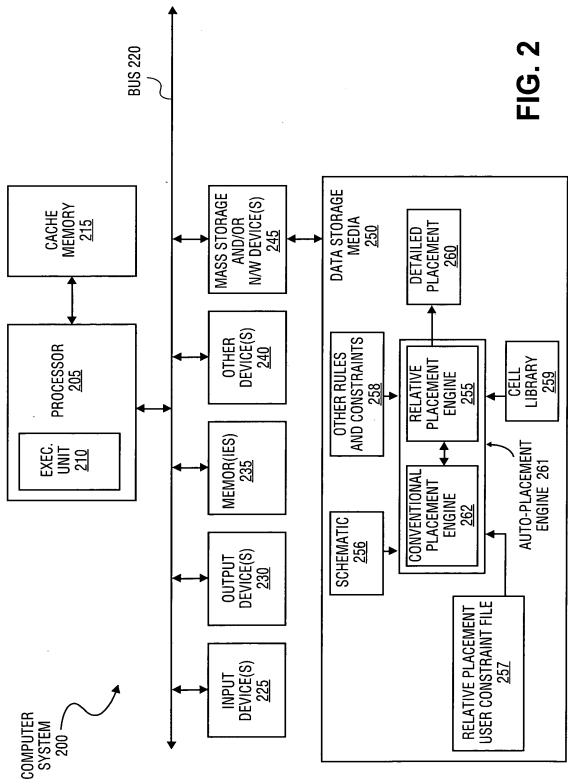
7







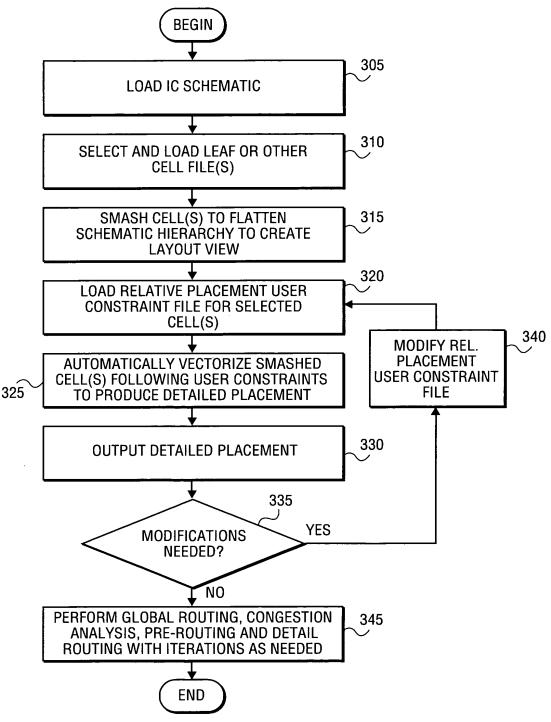


FIG. 3



SPECIFICATION	IMPLICATION											
(1) V1 = vector{a(0:9)}(2) V2 = vector (stride 2) {b(0:4)}	a[0] ₩ Ç	 W.	a[2] % b[1] Q	Z		<i>W</i> .	 b[3	2		a[9]		
(3) $V3 = \text{vector}\{c(0,5,6:8) \ d(0:3:2)\}$	8 c[0]	※ c[5]		₩ c[7]			∰ d[2]					
(4) $V4 = vector{skip(2) j(0:1)}$												
(5) V5 = step{interleave{v3 v4} i[(0)]}	c[0]		₩ c[5]		⋘ c[6]	j[0]	\$\$ c[7]		583 C[8]	∰ d[0]	# d[2]	∭ i[0
(6) V6 = merge{V1 V2}	Q M	<i>‰</i>	Ç; <u>∭</u> Ma	cro	1	i .	Q Z	%	Q:	W		
(7) V7 = interleave {vector{Tall(0:4)} merge{vector{x(0:4)} vector{y(0:4)}		Wacr	4 .				-	oth	er			
(8) hg = Vabut{V6 step{v7 zzinst}}			Q	nen XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX					CW.		nst	
	0	1	2		Bits		6 > ,					

FIG. 4



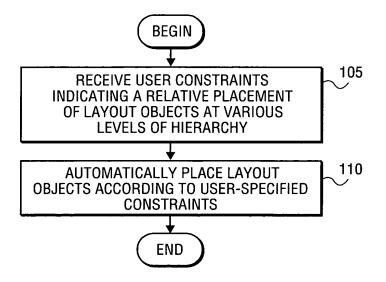


FIG. 1

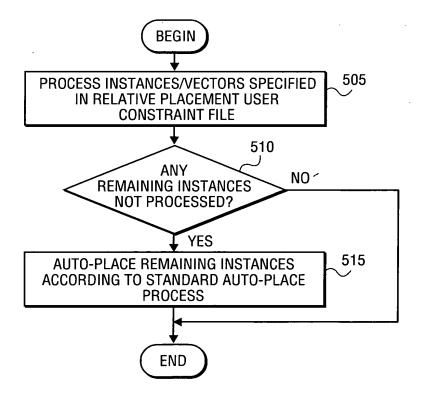


FIG. 5



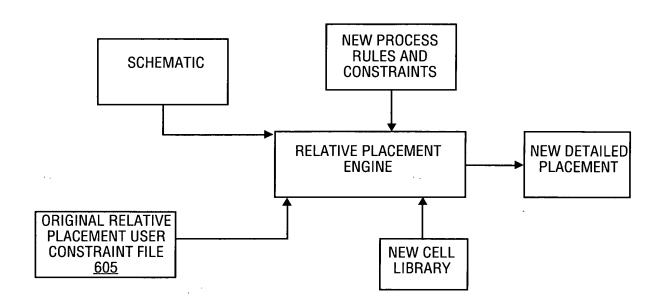


FIG. 6



